



PATRONS

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(HOD Computer)

Prof. Shankar N. Isal
(HOD Electronics)

Event Co-ordinator

Prof. Umesh Patil (9619058856)
Email: dypp.techsem@gmail.com

Programme Co-ordinators

Computer Group:

1. Prof. Madhuri Chavan (9820443969)
2. Prof. Pranita Sangit (9833819752)

Electronics Group:

1. Prof. Amit Gawande (9860263206)
2. Prof. Laxman Sonnur (9773982618)



ABOUT THE INSTITUTE:-

Dr. D. Y. Patil Polytechnic was established in the year 2000-2001 and we are the part of huge educational society comprising of 20 institutes imparting education in Technology & Management, Medical Science etc.

In the last Fifteen years, the Institute has emerged as a recognized Centre of excellence in the field of technical education. The courses are accredited by NBA.

The institute is recognized by the All India Council for Technical Education (AICTE) for diploma courses in Engineering & the permission to set up the institute was granted by the Govt. of Maharashtra, Director of Technical Education.

THE INSTITUTE IS RUNNING THE FOLLOWING COURSES:

- 1) Computer Engineering (Accredited)
- 2) Electronics & Communication Engg.
- 3) Electronics Engineering (Accredited)
- 4) Information Technology

OBJECTIVE OF THE CONFERENCE:

- To provide a platform to the final year diploma students to exhibit their technical talent in the form of innovative paper.
- To update their knowledge in the Educational technologies.
- To create a synergy between educational institutes & Industry.



D Y PATIL
GROUP

DYP-TECHNO-SEM 2015

STATE LEVEL PAPER PRESENTATION COMPETITION

(24TH February 2015)

FOR COMPUTER GROUP & ELECTRONICS GROUP

Strengthening the bridge among Institute,
Industry and Society

SPONSORED BY



M.S.B.T.E

ORGANIZED BY

DR. D.Y. PATIL POLYTECHNIC (0548)

SECTOR-7, PHASE-1, NERUL

NAVI MUMBAI - 400706

Ph. Nos.-(022)-27709592, 65251495

Web Site: - www.dyatilpolytechnic.ac.in

TOPICS :-

Group A : COMPUTER

- Cloud Computing
- Cyber Security
- Mobile Computing
- Computer Forensics
- Artificial Intelligence

Co-ordinator :

Prof. Pranita Sangit : 09833819752

E-mail :dypp.techsem@gmail.com

Group B : ELECTRONICS

- VLSI
- Process Control
- Embedded System
- Mobile Communication
- PLC/SCADA
- Video Signal Processing

Co-ordinator :

Prof. Amit Gawande : 09860263206

E-mail :dypp.techsem@gmail.com

Maximum members per group : Two

IMPORTANT DATES :-

Submission of full paper to respective co-ordinators given above :-

Last Date of Entry - **16th Feb 2015**

Confirmation Date - **20th Feb 2015**

Competition Date - **24th Feb 2015**

NOTE :(For selected Entry)

Participants should carry their ID-card at the time of Event

Participants should report **between 8.00 to 8.30 am on 24th Feb 2015**

TERMS & CONDITIONS :-

- Bring three hard copies of technical paper along with soft copy in CD at the time of presentation. Also send full length papers soft copy in IEEE format on or before 16th Feb 2015.
- Paper should not be more than 300 words. Mention clearly name of College, department name, title of paper, name of participants on first page.
- There shall not be more than two members per paper.
- Time duration is 10 minutes for presentation and 3 minutes for questioning.
- Selected papers for presentation will be communicated on or before 20th Feb, 2015.

PRIZES & TROPHY

FIRST PRIZE :
Rs. 3000+TROPHY

SECOND PRIZE :
Rs. 1000+TROPHY

THIRD PRIZE :
TROPHY

Participation Certificate will be given to every selected participants.

REGISTRATION FEES:-

Rs.250/- D.D. by each group in favour of "DR. D. Y. PATIL POLYTECHNIC NERUL"

Registration Form

DYP – TECHNO-SEM-2015

24th February, 2015

Title of Paper Presentation:-

Name of Participants & Contact No.

1) _____
E-mail:- _____ Sign:- _____

2) _____
E-mail:- _____ Sign:- _____

Class:- _____ Branch:- _____

Participating Institute Details :-

Name (Poly)_____

Phone No.:_____

D.D. No.:_____ Date:-_____

Name of Bank:_____

D.D. of Rs. 250/- drawn in favour of

"Dr. D. Y Patil Polytechnic, Nerul"

Note - Original DD Should be enclosed

Seal

Sign of Principal