XILINX®

PCI Target System Module
for XILINX XC4000E FPGA

Features

- Fully-verified and fully-implemented 33 MHz PCI Target interface in 40% of an XC4013E-3PQ208C FPGA.

- Verified electrical and functional PCI compliance using industry standard test suites.

- Single-chip solution integrates the PCI Target interface, configuration registers, burst FIFOs, and a custom back-end interface.

- Scaleable design fits into a range of XC4000E FPGA devices. Tailor your design for maximum cost-effectiveness.

- Two Base Address Registers (BARs).

- 16-deep x 32-wide burst FIFOs provide the highest data transfer performance of any programmable logic solution.

- Easy migration to Xilinx XC4400 HardWire™ for low-cost, high-volume applications.

- Complete VIEWlogic schematic design files and simulation command files available under non-disclosure. Saves months of development time and cost.

Application Procedure

The Xilinx PCI Target System Module ensures fast time-to-volume by providing a complete design solution. The schematic designs, the simulation command files, and the verification process saves thousands of dollars and many months of engineering time.

The PCI System Module is still under development. It has been verified functionally and is undergoing full PCI timing compliance testing.

The PCI System Module is provided under license from XILINX for use in Xilinx programmable logic devices and XILINX HardWire™ gate arrays.

Until the module is widely released, it is only available to a limited number of customers. A signed License Agreement and Non-Disclosure Agreement are required to obtain the PCI Target System Module.

To apply for the PCI Target System Module design, please

- Complete the attached PCI Target System Module Application Form.

- Review and sign the attached PCI Target System Module License Agreement.

- Review and sign a XILINX Non-Disclosure Agreement.

Contacting Xilinx PCI Applications

E-mail

Contact Xilinx PCI Applications via E-mail at:

pci@xilinx.com

FAX

Contact Xilinx PCI Applications via FAX at

1−408−879−4442

Please address all correspondence to "PCI Applications: Target Design".

When completed, please FAX all forms to 1-408-879-4442.

PRELIMINARY−SUBJECT TO CHANGE

PCI TARGET SYSTEM MODULE APPLICATION FORM

Name: _____ **Mail Stop:** _____

Title: _____ **Address:** _____

Company: _____ _____

Telephone: _____ **City:** _____

FAX: _____ **State:** _____

E-mail: _____ **Postal Code:** _____

Country: _____

What is your PCI application?

_____

_____

Which development platform do you use?

☐ PC (DOS) ☐ PC (Windows)

☐ SPARC ☐ HP 700 ☐ RS6000

Do you currently own a Xilinx development system (Version 5.1 or later) for XC4000 FPGAs and is your software currently under a maintenance contract?

☐ Yes ☐ No

Do you currently use VIEWdraw and VIEWsim when designing with XILINX FPGAs?

☐ Yes ☐ No

What is your preferred development environment?

_____

_____

Which type of PCI interface are you planning to eventually use?

☐ Target-Only ☐ Target/Initiator

What is the bus frequency for your application?

_____ MHz

What other PCI interface solutions are you evaluating?

_____

_____

What functions does your back-end interface require?

_____

_____

_____

_____

Will your back-end interface use the burst FIFOs, or will it be a pass-through interface?

☐ Burst FIFOs ☐ Pass-Through

If your design will use the burst FIFOs, what is your burst transfer size?

_____ words

As a beta site user, I am willing to *(check all that apply)***:**

☐ Provide immediate feedback on any issues or problems that I encounter using the PCI Target macro.

☐ Write a final report describing any problems or issues that I encountered using the PCI Target macro.

☐ Purchase a software maintenance contract if my system is not currently under a support agreement.

XILINX PCI TARGET AND INITIATOR SYSTEM MODULE LICENSE AGREEMENT

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I have read and understand all of the PCI Target or Initiator System Module License Agreement.

Name: _____

(Signed)   *(Date)*

Printed: _____

Title: _____

Company: _____

Address: _____

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