



# **SAMRAT ASHOK TECHNOLOGICAL INSTITUTE**

(Engineering College)

(Established in 1960)

**VIDISHA (M.P.) - 464 001**

**सम्राट अशोक अभियांत्रिकीय संस्थान**

(इन्जीनियरिंग महाविद्यालय)

(1960 में स्थापित)

**विदिशा (म.प्र.) - 464 001**

A grant-in-aid, autonomous Institution under the Govt. of M. P.

(Approved by All India Council for Technical Education and affiliated to Rajiv Gandhi Proudhyogiki Vishwavidyalaya, Bhopal)

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## **INVITATION FOR QUOTATION**

TEQIP-II/2015/MP1G02/Shopping/ ----- /Q

**21-Jan.-2016**

To,

**Sub: Invitation for Quotations for supply of Goods**

Dear Sir,

1. You are invited to submit your most competitive quotation for the following goods with item wise detailed specifications given at Annexure I,

Sr. No	Brief Description	Quantity	Delivery Period(In days)	Place of Delivery	Installation Requirement (if any)
1	Function generator	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	
2	P-N junction diode kit	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	
3	Zener diode kit	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	
4	Transistor	1	30	Samrat Ashok Technological	

	characteristics kit			Institute, Engg. College Vidisha (M.P.) 464001	
5	FET Characteristics kit	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	
6	Logic gate kit	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	
7	Verification of Boolean equations kit	1	30	Samrat Ashok Technological Institute, Engg. College Vidisha (M.P.) 464001	

2. Government of India has received a credit from the International Development Association (IDA) towards the cost of the **Technical Education Quality Improvement Programme [TEQIP]-Phase II** Project and intends to apply part of the proceeds of this credit to eligible payments under the contract for which this invitation for quotations is issued.

3. Quotation,

3.1 The contract shall be for the full quantity as described above.

3.2 Corrections, if any, shall be made by crossing out, initialing, dating and re writing.

3.3 All duties and other levies payable by the supplier under the contract shall be included in the unit price.

3.4 Applicable taxes shall be quoted separately for all items.

3.5 The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.

3.6 The Prices should be quoted in Indian Rupees only.

4. Each bidder shall submit only one quotation. **Quotation must be submitted in format given at the end of this letter.**

5. Relevant and valid product dealership certificate /marketing authorization letter from principals/OEM ,if any ,must be accompanied with the quotations.

6. Quotation shall remain valid for a period not less than **45** days after the last date of quotation submission.

7. Evaluation of Quotations,

The Purchaser will evaluate and compare the quotations determined to be substantially responsive i.e. which

- 7.1 are properly signed ; and
- 7.2 confirm to the terms and conditions, and specifications.
8. The Quotations would be evaluated for all items together.
9. Award of contract:
- The Purchaser will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price.
- 9.1 Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of contract.
- 9.2 The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be incorporated in the purchase order.
10. Payment shall be made in Indian Rupees as follows:
- Delivery and Installation - 100% of total cost**
- Satisfactory Acceptance Delivery and Installation - 0% of total cost**
11. All supplied items are under warranty of **12 months** from the date of successful acceptance of items.
12. You are requested to provide your offer latest by **15:00 hours on 06-Feb.-2016 .**
13. Please mention following at the sealed envelope containing the proposal:
- PackageName: Analog Electronics and Linear IC's Lab**
- Package Number: TEQIP-II/MP/MP1G02/333**
- DO NOT OPEN BEFORE 15:00 Hrs of 06-Feb.-2016.**
14. Detailed specifications of the items are at Annexure I.
15. Training Clause (if any) **YES**
16. Testing/Installation Clause (if any) **YES**
17. Information brochures/ Product catalogue, if any must be accompanied with the quotation clearly indicating the model quoted for.
18. Institute will not pay any charges for installation, demonstration and training of supplied items.
19. Supplier must specify the make and model of quoted items.

20. Sealed quotation to be submitted/ delivered at the address mentioned below,

**Director**

**Office of the Director**

**Samrat Ashok Technological Institute (Engg. College), Civil Lines, Vidisha (M.P.)**

**464001 India**

21. We look forward to receiving your quotation and thank you for your interest in this project.



**(Dr. J. S. Chauhan)**



**Director**



### Annexure I

S. No.	Name of the Equipment.	Specifications
1.	Function generator kit	sine, square and triangular wave shape, output voltage 0-20 Vp/p, frequency range 0.1 MHz to 1 MHz in 6 range.
2.	P-N junction diode kit	Two meters be provided for measurement of volts & current of diode in forward as well as reverse direction. Typical value of reverse breakdown voltage should be mentioned.
3.	Zener diode kit	Two meters be provided for measurement of volts & current of diode in forward as well as reverse direction. Maximum zener current & zener voltage of diode be designed.
4.	Transistor characteristics kit.	Independent power supply of base-emitter junction and collector emitter junction, Three meters (measurements of volts and current) along with transistor (NPN, PNP) on front panel for input and output characteristics.
5.	FET characteristics kit	Measurement of statics & transfer characteristics of FET, dual power supply, three meters to read different parameters.
6.	Logic gate kit	Using discrete components of OR, AND, NAND & NOT gates be mounted & printed for working of DTL gates and to verify the truth table, NOR, EX OR & EX NOR can be assembled by combination of gates.
7.	Verification of Boolean equations kit	Using different combination 5-OR, 5-AND, 4-NOT gate for boolean identities & Demorgan's theorems can be assembled and verified.

**FORMAT FOR QUOTATION SUBMISSION**

(In letterhead of the supplier with seal)

Date: \_\_\_\_\_

To:

\_\_\_\_\_

\_\_\_\_\_

Sl. No.	Description of goods (with full Specifications)	Qty.	Unit	Quoted Unit rate in Rs.  (Including Ex Factory price, excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and warranty/ guaranty commitments)	Total Price  (A)	Sales tax and other taxes payable	
						In  %	In figures  (B)
Total Cost							

Gross Total Cost (A+B): Rs. \_\_\_\_\_

We agree to supply the above goods in accordance with the technical specifications for a total contract price of Rs. \_\_\_\_\_ (Amount in figures) (Rupees \_\_\_\_\_amount in words) within the period specified in the Invitation for Quotations.

We confirm that the normal commercial warranty/ guarantee of \_\_\_\_\_ months shall apply to the offered items and we also confirm to agree with terms and conditions as mentioned in the Invitation Letter.

We hereby certify that we have taken steps to ensure that no person acting for us or on our behalf will engage in bribery.

Signature of Supplier

Name: \_\_\_\_\_

Address: \_\_\_\_\_

Contact No: \_\_\_\_\_







