

MachXO2-1200ZE Breakout Board Evaluation Kit

User's Guide

Introduction

Thank you for choosing the Lattice Semiconductor MachXO2[™]-1200ZE Breakout Board Evaluation Kit!

This user's guide describes how to start using the MachXO2-1200ZE Breakout Board, an easy-to-use platform for evaluating and designing with the MachXO2-1200ZE PLD. Along with the board and accessories, this kit includes a pre-loaded demonstration design. You may also reprogram the on-board MachXO2-1200ZE device to review your own custom designs.

Note: Static electricity can severely shorten the lifespan of electronic components. See the Storage and Handling section of this document for handling and storage tips.

Features

The MachXO2-1200ZE Breakout Board Evaluation Kit includes:

- MachXO2-1200ZE Breakout Board The board is a 3" x 3" form factor that features the following on-board components and circuits:
 - MachXO2-1200ZE PLD (LCMXO2-1200ZE-1TG144C)
 - USB mini-B connector for power and programming
 - Eight LEDs
 - 60-hole prototype area
 - Four 2x20 expansion header landings for general I/O, JTAG, and external power
 - 1x8 expansion header landing for JTAG
 - 3.3V and 1.2V supply rails
- **Pre-loaded Demo** The kit includes a pre-loaded counter design that highlights use of the embedded MachXO2-1200ZE oscillator and programmable I/Os configured for LED drive.
- USB Connector Cable The board is powered from the USB mini-B socket when connected to a host PC. The USB channel also provides a programming interface to the LCMXO2-1200ZE JTAG port.
- Lattice Breakout Board Evaluation Kits Web Page Visit <u>www.latticesemi.com/breakoutboards</u> for the latest documentation (including this guide) and drivers for the kit.

The content of this user's guide includes demo operation, programming instructions, top-level functional descriptions of the Breakout Board, descriptions of the on-board connectors, and a complete set of schematics.





Storage and Handling

Static electricity can shorten the lifespan of electronic components. Please observe these tips to prevent damage that could occur from electro-static discharge:

- Use anti-static precautions such as operating on an anti-static mat and wearing an anti-static wrist-band.
- Store the evaluation board in the packaging provided.
- Touch a metal USB housing to equalize voltage potential between you and the board.

Software Requirements

You should install the following software before you begin developing new designs for the Breakout board:

- Lattice Diamond[®] design software
- FTDI Chip USB hardware drivers (installed as an option within the Diamond installation program)

MachXO2-1200ZE Device

This board features the MachXO2-1200ZE PLD which offers embedded Flash technology for instant-on, non-volatile operation in a single chip. Numerous system functions are included, such as a PLL and 64Kbits of embedded RAM plus hardened implementations of I²C, SPI, timer/counter, and user Flash memory. Flexible, high performance I/Os support numerous single-ended and differential standards including LVDS, and also source synchro-

Lattice Semiconductor

nous interfaces to DDR/DDR2/LPDDR DRAM memory. The 144-pin TQFP package provides 108 user I/Os in a 20mm x 20mm form factor. A complete description of this device can be found in the <u>MachXO2 Family Data Sheet</u>.

Demonstration Design

Lattice provides a simple, pre-programmed demo to illustrate basic operation of the MachXO2-1200ZE device. The design integrates an up-counter with the on-chip oscillator.

Note: You may obtain your Breakout Board after it has been reprogrammed. To restore the factory default demo and program it with other Lattice-supplied examples see the Download Demo Designs section of this document.

Run the Demonstration Design

Upon power-up, the preprogrammed demonstration design automatically loads and drives the LED array in an alternating pattern. The program shows a clock generator based on the MachXO2-1200ZE on-chip oscillator. The counter module is clocked at the oscillator default frequency of 2.08MHz to illustrate how low speed timer functions can be implemented with a PLD. The 22-bit up-counter further divides the clock to advance the LED display approximately every 500ms. The resulting light pattern will appear as an alternating pair of lit LEDs per row.

Figure 2. Demonstration Design Block Diagram



WARNING: Do not connect the Breakout Board to your PC before you follow the driver installation procedure of this section.

Communication with the Breakout Board with a PC via the USB connection cable requires installation of the FTDI chip USB hardware drivers. Loading these drivers enables the computer to recognize and program the Breakout Board. Drivers can be loaded as part of the installation of Lattice Diamond design software or Diamond Programmer, or as a stand-alone package.

To load the FTDI Chip USB hardware drivers as part of the Lattice Diamond installation:

- 1. Select **Programmer Drivers** in the Product Options of Lattice Diamond Setup.
- 2. Select FTDI Windows USB Driver or All Drivers in the LSC Drivers Install/Uninstall dialog box.
- 3. Click **Finish** to install the USB driver.
- 4. After the driver installation is complete, connect the USB cable from a USB port on your PC to the board's USB mini-B socket (J2). After the connection is made, a green Power LED (D9) will light indicating the board is powered on.
- 5. The demonstration design will automatically load and drive the LED array in an alternating pattern.

To load the FTDI chip USB hardware drivers via the stand-alone package:

- 1. Browse to <u>www.latticesemi.com/breakoutboards</u> and download the FTDI Chip USB Hardware Drivers package.
- 2. Extract the FTDI chip USB Hardware driver package to your PC hard drive.
- 3. Connect the USB cable from a USB port on your PC to the board's USB mini-B socket (J7). After the connection is made, a green Power LED (D9) will light indicating the board is powered on.
- 4. If you are prompted, "Windows may connect to Windows Update" select **No**, **not this time** from available options and click **Next** to proceed with the installation. Choose the **Install from specific location (Advanced)** option and click **Next**.
- Search for the best driver in these locations and click the Browse button to browse to the Windows driver folder created in the Download Windows USB Hardware Drivers section. Select the CDM 2.04.06 WHQL Certified folder and click OK.
- 6. Click **Next**. A screen will display as Windows copies the required driver files. Windows will display a message indicating that the installation was successful.
- 7. Click **Finish** to install the USB driver.
- 8. The demonstration design will automatically load and drive the LED array in an alternating pattern.

See the Troubleshooting section of this guide if the board does not function as expected.

Download Demo Designs

The counter demo is preprogrammed into the Breakout Board, however over time it is likely your board will be modified. Lattice distributes source and programming files for demonstration designs compatible with the Breakout Board.

To download demo designs:

- 1. Browse to the Lattice Breakout Board Evaluation Kits web page (<u>www.latticesemi.com/breakoutboards</u>) of the Lattice web site. Select **MachXO2 Breakout Board Demo Source** and save the file.
- Extract the contents of MachXO2_1200ZE_BB_Eval_Kit_v01.0.zip to an accessible location on your hard drive.

The demo design directory **Demo_LED** is unpacked with all design files needed for the demo, including the JEDEC programming data file.

Continue to Programming a Demo Design with Lattice Diamond Design Software.

Programming a Demo Design with the Lattice Diamond Programmer

The demonstration design is pre-programmed into the MachXO2-1200ZE Breakout Board by Lattice. If you have changed the design but now want to restore the Breakout Board to factory settings, use the procedure described below.

To program the MachXO2-1200ZE device:

- 1. Install, license and run Lattice Diamond software. See www.latticesemi.com/latticediamond for download and licensing information.
- 2. Connect the USB cable to the host PC and the MachXO2-1200ZE Breakout Board.
- 3. From Diamond, open the **Demo_LED_OSC.Idf** project file.

Lattice Semiconductor

- 4. Click the **Programmer** icon.
- 5. Click Detect Cable. The Programmer will detect the cable (Cable: USB2, Port: FTUSB-0).
- 6. Click the **Program** icon. When complete, **PASS** is displayed in the Status column.

MachXO2-1200ZE Breakout Board

This section describes the features of the MachXO2-1200ZE Breakout Board in detail.

Overview

The Breakout Board is a complete development platform for the MachXO2-1200ZE PLD. The board includes a prototyping area, a USB program/power port, an LED array, and header landings with electrical connections to most of the PLD's programmable I/O, power, and JTAG pins. The board is powered by the PC's USB port or optionally with external power. You may create or modify the program files and reprogram the board using Lattice Diamond software.

Figure 3. MachXO2-1200ZE Breakout Board Block Diagram



Table 1 describes the components on the board and the interfaces it supports.

Component/Interface	Туре	Schematic Reference	Description
Circuits		·	
USB Controller	Circuit	U2: FT2232H	USB-to-JTAG interface and dual USB UART/FIFO IC
USB Mini-B Socket	I/O	J7:USB_MINI_B	Programming and debug interface
Components			
LCMXO2-1200ZE	PLD	U3: LCMXO2- 1200ZE-1TG144C	1200-LUT device packaged in a 20 x 20mm, 144-pin TQFP
Interfaces		·	
LED Array	Output	D8-D1	Red LEDs
Four 2x20 Header Landings	I/O	J2: header_2x20 J3: header_2x20 J4: header_2x20 J5: header_2x20	User-definable I/O
1x8 Header Landing	I/O	J1: header_1x8	Optional JTAG interface
4x15 60-Hole Prototype Area			Prototype area 100mil centered holes.
Test points	Power	TP1: +3.3V TP2: +1.2V TP3: GND	Power and ground reference points

Subsystems

This section describes the principle sub systems for the Breakout Board in alphabetical order.

Clock Sources

All clocks for the counter demonstration designs originate from the MachXO2-1200ZE PLD on-chip oscillator. You may use an expansion header landing to drive a PLD input with an external clock source.

Expansion Header Landings

The expansion header landings provide access to user GPIOs, primary inputs, clocks, and VCCO pins of the MachXO2-1200ZE. The remaining pins serve as power supplies for external connections. Each landing is configured as one 2x20 100 mil.

Table 2. Expansion Connector Reference

ltem	Description
Reference Designators	J2, J3, J4, J5
Part Number	header_2x20

 Table 3. Expansion Header Pin Information (J2)

Pin Number	Function	MachXO2-1200ZE Pin
1	NC	N/A
2	VCCIO0	118, 123, 135
3	PT17D/DONE	109
4	PT17C/INITn	110
5	PT17B	111
6	PT17A	112
7	GND	N/A
8	GND	N/A
9	PT16D	113
10	PT16C	114
11	PT16B	115
12	PT16A	117
13	PT15D/PROGn	119
14	PT15C/JTAGen	120
15	GND	N/A
16	GND	N/A
17	PT15B	121
18	PT15A	122
19	PT12D/SDA/PCLKC0_0	125
20	PT12C/SCL/PCLKT0_0	126
21	PT12B/PCLKC0_1	127
22	PT12A/PCLKT0_1	128
23	GND	N/A
24	GND	N/A
25	PT11D/TMS	130
26	PT11C/TCK/TEST_CLK	131
27	PT11B	132
28	PT11A	133
29	PT10D/TDI	136
30	PT10C/TDO	137
31	GND	N/A
32	GND	N/A
33	PT10B	138
34	PT10A	139
35	PT9D	140
36	PT9C	141
37	PT9B	142
38	PT9A	143
39	GND	N/A
40	GND	N/A

 Table 4. Expansion Header Pin Information (J3)

Pin Number	Function	MachXO2-1200ZE Pin
1	VCC_1.2V	36, 72, 108, 144
2	VCCIO1	79, 88, 102
3	VCC_1.2V	36, 72, 108, 144
4	NC	N/A
5	PR10C	74
6	PR10D	73
7	PR10A	76
8	PR10B	75
9	GND	N/A
10	GND	N/A
11	PR9C	78
12	PR9D	77
13	PR9A	82
14	PR9B	81
15	GND	N/A
16	GND	N/A
17	PR8C	84
18	PR8D	83
19	PR8A	86
20	PR8B	85
21	GND	N/A
22	GND	N/A
23	PCLKT1_0/PR5C	92
24	PCLKC1_0/PR5D	91
25	PR5A	94
26	PR5B	93
27	GND	N/A
28	GND	N/A
29	PR4C	96
30	PR4D	95
31	PR4A	98
32	PR4B	97
33	GND	N/A
34	GND	N/A
35	PR3A	100
36	PR3B	99
37	PR2C	105
38	PR2D	104
39	PR2A	107
40	PR2B	106

Table 5. Expansion Header Pin Information (J4)

Pin Number	Function	MachXO2-1200ZE Pin
1	VCC_3.3V	N/A
2	VCCIO3	7, 16, 30
3	VCC_3.3V	N/A
4	NC	N/A
5	PL2A/L_GPLLT_FB	1
6	PL2B/L_GPPLC_FB	2
7	PL2C/L_GPLLT_IN	3
8	PL2D/L_GPLLC_IN	4
9	PL3A/PCLKT3_2	5
10	PL3B/PCLKC3_2	6
11	PL3C	9
12	PL3D	10
13	GND	N/A
14	GND	N/A
15	PL4A	11
16	PL4B	12
17	PL4C	13
18	PL4D	14
19	GND	N/A
20	GND	N/A
21	PL5A/PCLKT3_1	19
22	PL5B/PCLKC3_1	20
23	PL5C	21
24	PL5D	22
25	GND	N/A
26	GND	N/A
27	PL8A	23
28	PL8B	24
29	PL8C	25
30	PL8D	26
31	GND	N/A
32	GND	N/A
33	PL9A/PCLKT3_0	27
34	PL9B/PCLKC3_0	28
35	GND	N/A
36	GND	N/A
37	PL10A	32
38	PL10B	33
39	PL10C	34
40	PL10D	35

 Table 6. Expansion Header Pin Information (J5)

Pin Number	Function	MachXO2-1200ZE Pin
1	NC	N/A
2	VCCIO2	37, 51, 66
3	SI/SISPI/PB20D	71
4	PB20B	69
5	SN/PB20C	70
6	PB20A	68
7	PB18D	67
8	PB18B	62
9	PB18C	65
10	PB18A	61
11	GND	N/A
12	GND	N/A
13	PB15D	60
14	PB15B	58
15	PB15C	59
16	PB15A	57
17	GND	N/A
18	GND	N/A
19	PCLKC2_1/PB11B	56
20	PB11D	54
21	PCLKT2_1/PB11A	55
22	PB11C	52
23	GND	N/A
24	GND	N/A
25	PCLKC2_0/PB9B	50
26	PB9D	48
27	PCLKT2_0/PB9A	49
28	PB9C	47
29	GND	N/A
30	GND	N/A
31	S0/SPISO/PB6D	45
32	PB6B	43
33	MCLK/CLK/PB6C	44
34	PB6A	42
35	GND	N/A
36	GND	N/A
37	PB4D	41
38	PB4B	39
39	CSSPIN/PB4C	40
40	PB4A	38





Figure 5. J3/J5 Header Landing Callout







MachXO2-1200ZE PLD

The MachXO2-1200ZE-1TG144C is a 144-pin TQFP package PLD device which provides 108 I/Os in a 20 x 20mm package.

ltem	Description
Reference Designators	U3
Part Number	LCMXO2-1200ZE-1TG144C
Manufacturer	Lattice Semiconductor
Web Site	www.latticesemi.com

JTAG Interface Circuits

For power and programming an FTDI USB UART/FIFO IC converter provides a communication interface between a PC host and the JTAG programming chain of the Breakout Board. The USB 5V supply is also used as a source for the 3.3V supply rail. A USB mini-B socket is provided for the USB connector cable.

Table 8. JTAG Interface Reference

ltem	Description
Reference Designators	U1
Part Number	FT2232HL
Manufacturer	Future Technology Devices International (FTDI)
Web Site	www.ftdichip.com

Table 9. JTAG Programming Pin Information

Description	MachXO2-1200ZE Pin
Test Data Output	137:TDO
Test Data Input	136:TDI
Test Mode Select	130:TMS
Test Clock	131:TCK

LEDs

A green LED (D9) is used to indicate USB 5V power. Eight red LEDs are driven by I/O pins of the MachXO2-1200ZE device.

Table 10. Power and User LEDs Reference

ltem	Description
Reference Designators	D1, D2, D3, D4, D5, D6, D7, D8, D9
Part Number	LTST-C190KRKT (D1-D8) LTST-C190KGKT (D9)
Manufacturer	Lite-On It Corporation
Web Site	www.liteonit.com

Power Supply

3.3V and 1.2V power supply rails are converted from the USB 5V interface when the board is connected to a host PC.

Test Points

In order to check the various voltage levels used, test points are provided:

- TP1: +3.3V
- TP2: +1.2V
- TP3: GND

USB Programming and Debug Interface

The USB mini-B socket of the Breakout Board serves as the programming and debug interface.

<u>JTAG Programming</u>: For JTAG programming, a preprogrammed USB PHY peripheral controller is provided on the Breakout Board to serve as the programming interface to the MachXO2-1200ZE PLD.

Programming requires the Lattice Diamond or ispVM System software.

Table 11. USB Interface Reference

ltem	Description
Reference Designators	U1
Part Number	FT2232HL
Manufacturer	Future Technology Devices International (FTDI)
Web Site	www.ftdichip.com

Board Modifications

This section describes modifications to the board to change or add functionality.

Bypassing the USB Programming Interface

The USB programming interface circuit (USB Programming and Debug Interface section) may be optionally bypassed by removing the 0 ohm resistors: R5, R6, R7, R8 (See Appendix A. Schematics, Sheet 2 of 5). Header landing J1 provides JTAG signal access for jumper wires or a 1x8 pin header.

Applying External Power

The Breakout Board is powered by the circuit of Schematic Sheet 5 of 5 based on the 5V USB power source. You may disconnect this power source by removing the 0 ohm resistors: R42 (VCC_1.2V) and R44 (VCC_3.3V). Power connections are available from the expansion header landings, J3 (+1.2V, pins 1 and 3, schematic sheet 3 of 5) and J4 (+3.3V, pins 1 and 3, schematic sheet 4 of 5).

Measuring Bank and Core Power

In addition to the expansion headers, test points (TP1, TP2) provide access to power supplies of the MachXO2-1200ZE PLD. Inline 1 ohm resistors: R24 (VCCIO0, +3.3V, Bank 0), R25 (VCCIO1, +3.3V, Bank 1), R26 (VCCIO2, +3.3V, Bank 2), R27 (VCCIO3, +3.3V, Bank 3), R56 (VCC core, +1.2V) can be used to measure current for the power supplies.

Mechanical Specifications

Dimensions: 3 in. [L] x 3 in. [W] x 1/2 in. [H]

Environmental Requirements

The evaluation board must be stored between -40° C and 100° C. The recommended operating temperature is between 0° C and 90° C.

The board can be damaged without proper anti-static handling.

Glossary

PLD: Programmable Logic Device

DIP: Dual in-line package

LED: Light Emitting Diode.

LUT: Look Up Table

PCB: Printed Circuit Board

RoHS: Restriction of Hazardous Substances Directive

USB: Universal Serial Bus

WDT: Watchdog Timer

Troubleshooting

Use the tips in this section to diagnose problems with the Breakout Board.

LEDs Do Not Flash

If power is applied but the board does not flash according to the preprogrammed counter demonstration then it is likely the board has been reprogrammed with a new design. Follow the directions in the Demonstration Design section to restore the factory default.

USB Cable Not Detected

If Lattice Diamond Programmer or ispVM System does not recognize the USB cable after installing the Lattice USB port drivers and rebooting, the incorrect USB driver may have been installed. This usually occurs if you attach the board to your PC prior to installing the Lattice-supplied USB driver.

To access the Troubleshooting the USB Driver Installation Guide:

For Diamond software and standalone Diamond Programmer:

- 1. Start Diamond or Diamond Programmer and choose Help.
- 2. Search for USB driver or Troubleshooting, then select the Troubleshooting the USB Driver topic.
- 3. Follow the directions to install the Lattice USB driver.

For ispVM:

- 1. Start ispVM System and choose **Options > Cable and I/O Port Setup**. The Cable and I/O Port Setup Dialog appears.
- 2. Click the **Troubleshooting the USB Driver Installation Guide** link. The *Troubleshooting the USB Driver Installation Guide* document appears in your system's PDF file reader.
- 3. Follow the directions to install the Lattice USB driver.

Determine the Source of a Pre-Programmed Device

If the Breakout Board has been reprogrammed, the original demo design can be restored. To restore the board to the factory default, see the Download Demo Designs section for details on downloading and reprogramming the device.

Ordering Information

Description	Ordering Part Number	China RoHS Environment-Friendly Use Period (EFUP)
MachXO2-1200ZE Breakout Board Evaluation Kit	LCMXO2-1200ZE-B-EVN	O

Technical Support Assistance

- Hotline: 1-800-LATTICE (North America)
 - +1-503-268-8001 (Outside North America)

e-mail: techsupport@latticesemi.com

Internet: <u>www.latticesemi.com</u>

Revision History

Date	Version	Change Summary
December 2011	01.0	Initial release.
January 2012	01.1	Figure "MachXO2-1200ZE Breakout Board, Top Side" updated with revision B board photo.

© 2012 Lattice Semiconductor Corp. All Lattice trademarks, registered trademarks, patents, and disclaimers are as listed at <u>www.latticesemi.com/legal</u>. All other brand or product names are trademarks or registered trademarks of their respective holders. The specifications and information herein are subject to change without notice.